Document Title	M084GNS1 R1 Product Specification			Page No.	1/27
Document No.		Issue date	2015/10/19	Revision	00

IVO Product Specification

To:

Product Name: M084GNS1 R1

Document Issue Date: 2015/10/19

Customer	InfoVision Optoelectronics
SIGNATURE _____ _____ _____ Please return 1 copy for your confirmation with your signature and comments.	**SIGNATURE** **REVIEWED BY CQM** _____ **PREPARED BY FAE** _____

Note : 1. Please contact InfoVision Company before designing your product based on this product.

2. The information contained herein is presented merely to indicate the characteristics and performance of our products. No responsibility is assumed by IVO for any intellectual property claims or other problems that may result from application based on the module described herein.

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Revised Record				
Ver	Date	Revised Content/Summary	Page	Revised By
00	2015/10/19	Tentative	All	yujia Yang

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1.0 General Descriptions

1.1 Introduction

The M084GNS1 R1 is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driver circuit and a backlight system. This TFT LCD has a 8.4 inch diagonally measured active display area with SVGA resolution (800 horizontal by 600 vertical pixels array).

1.2 Features

- Supported SVGA Resolution
- LVDS Interface
- Compatible with RoHS Standard

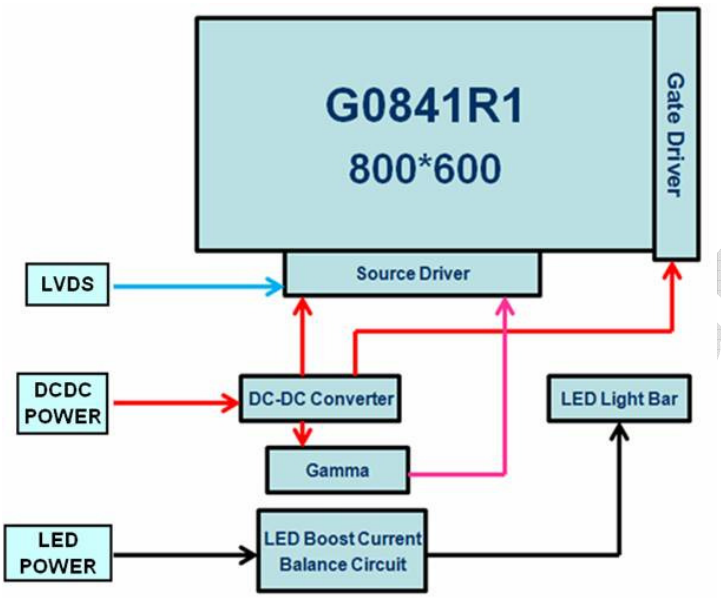
1.3 Product Summary

Items	Specifications	Unit
Screen Diagonal	8.4	inch
Active Area (H x V)	170.4H) x127.8(V)	mm
Number of Pixels (H x V)	800x 600	-
Pixel Pitch (H x V)	0.213 x 0.213	mm
Pixel Arrangement	R.G.B. Vertical Stripe	-
Display Mode	Normally White	-
White Luminance	(350) (Typ.)	cd /m^2
Contrast Ratio	(600) (Typ.)	-
Response Time	(16) (Typ.)	ms
Input Voltage	3.3 (Typ.)	V
Power Consumption	(2.70)(Max)	W
Weight	(200)(Typ)	g
Outline Dimension (H x V x D)	(203. 0) (Typ.) x (142.5) (Typ.) x (5.7) (Typ.)	mm
Electrical Interface (Logic)	LVDS	-
Support Color	262 K/16.7 M	-
NTSC	(45) (Typ.)	%
Viewing Direction	12 O'clock	-
Surface Treatment	Anti-glare	-

1.4 Functional Block Diagram

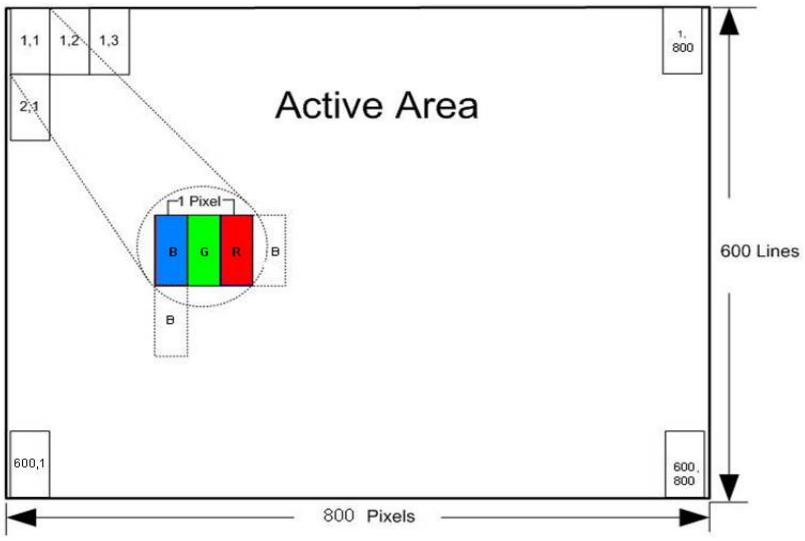
Figure 1 shows the functional block diagram of the LCD module.

Figure 1 Block Diagram



1.5 Pixel Mapping

Figure2 Pixel Mapping



2.0 Absolute Maximum Ratings

Table 1 Electrical & Environment Absolute Rating

Item	Symbol	Min.	Max.	Unit	Note
Logic Supply Voltage	V_{DD}	2.5	3.6	V	(1),(2)
Logic Input Signal Voltage	V_{Signal}	0	3.6	V	
Operating Temperature	TOP	-20	70	℃	(3),(4),(5),(6)
Storage Temperature	TST	-30	80	℃	
Vibration(Non-operating)	VB	-	100	G	(7)
Shock(Non-operating)	Shock	-	2.9	G	(8)

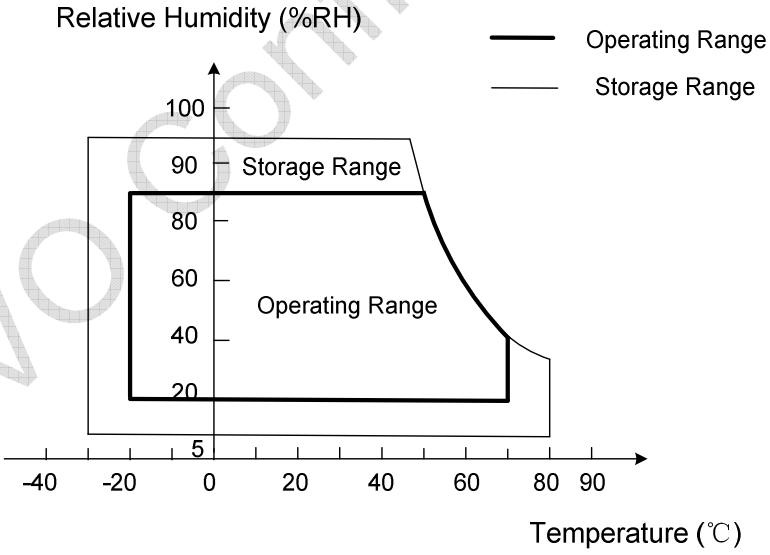
Note (1) Permanent damage may occur to the LCD module if beyond this specification. Functional operation should be restricted to the conditions described under normal operating conditions.

Note (2) Operating temperature 25℃, humidity 55%RH.

Note (3) (T<=40℃) Note static electricity.Maximum wet bulb temperature at 39℃ or less. (T>40℃) No condensation.

Note (4) There is a possibility of causing deterioration in the irregularity and others of the screen and the display fineness though the liquid crystal module doesn't arrive at destruction when using it at 50~60℃ or -20~0℃.

Figure 3 Absolute Ratings of Environment of the LCD Module



Note (5) There is a possibility of causing the fineness deterioration by the prolonged use in the (high temperature) humidity environment (60%RH or more).
Note (6) In the operating temperature item, the low temperature side is the ambient temperature regulations. The high temperature side is the panel surface temperature regulations.
Note (7) 10-500Hz, random vibration, 1hrs for X, Y, Z axis.
Note (8) 2ms, half sine wave, one time for X, Y, Z axis.

3.0 Optical Characteristics

The optical characteristics are measured under stable conditions as following notes.

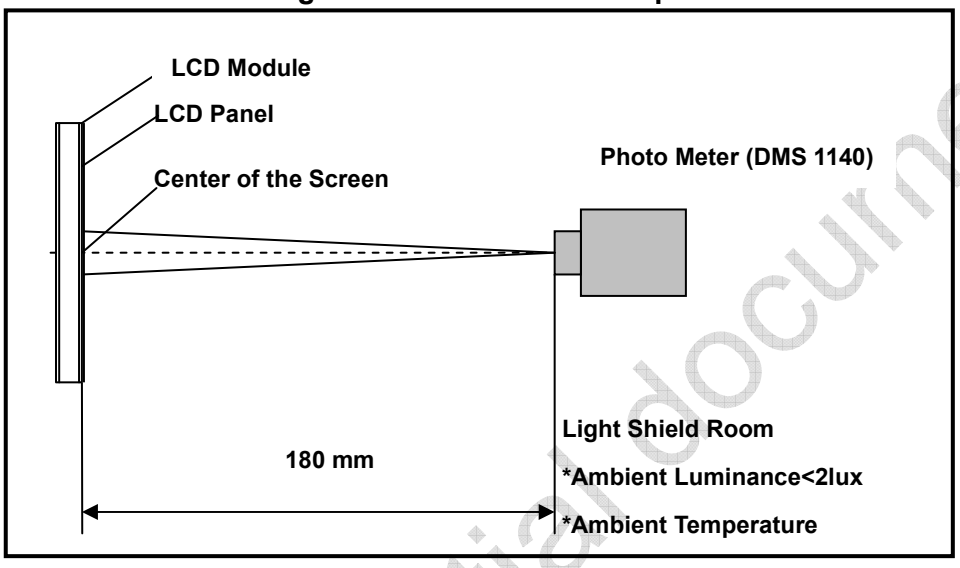
Table 2 Optical Characteristics

Item	Conditions		Min.	Typ.	Max.	Unit	Note
Viewing Angle （CR>10）	Horizontal	θ_{x+}	(70)	(80)	-	degree	(1),(2),(3)
		θ_{x-}	(70)	(80)	-		
	Vertical	θ_{y+}	(70)	(80)	-		
		θ_{y-}	(50)	(60)	-		
Contrast Ratio	Center		(480)	(600)	-	-	(1),(2),(4) θx=θy=0°
Response Time	Rising + Falling		-	(16)	(25)	ms	(1),(2),(5) θx=θy=0°
Color Chromaticity (CIE1931)	Red x		TBD	TBD	TBD	-	(1),(2),(3) θx=θy=0°
	Red y			TBD		-	
	Green x			TBD		-	
	Green y			TBD		-	
	Blue x			TBD		-	
	Blue y			TBD		-	
	White x		(0.26)	(0.310)	(0.36)	-	
	White y		(0.28)	(0.330)	(0.38)	-	
NTSC	-		(42)	(45)	-	%	(1),(2),(3) θx=θy=0°
White Luminance	Center point		(280)	(350)	-	cd/m^2	(1),(2) θx=θy=0°
Luminance Uniformity	9 Points		(70)	(75)	-	%	(1),(2),(6) θx=θy=0°

Note (1) Measurement Setup:

The LCD module should be stabilized at given temperature(25℃) for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.

Figure 4 Measurement Setup



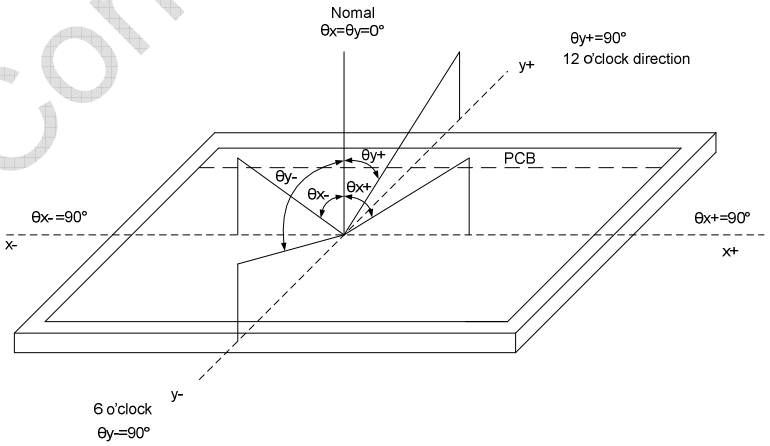
Note (2) The LED input parameter setting as:

I_LED: 68mA

PWM_LED: Duty 100 %

Note (3) Definition of Viewing Angle

Figure 5 Definition of Viewing Angle



Note (4) Definition Of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression:

6bit: Contrast Ratio (CR) = L63 / L0

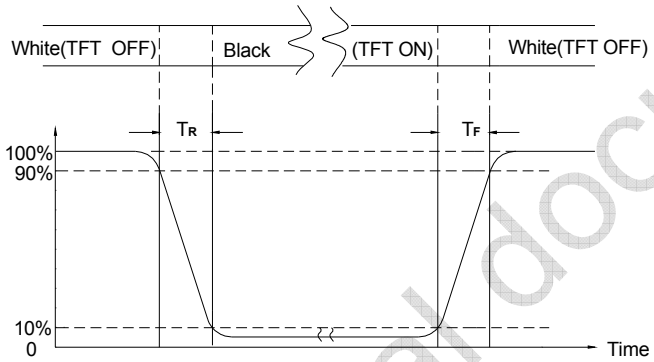
L63: Luminance of gray level 63, L0: Luminance of gray level 0

8bit: Contrast Ratio (CR) = L255 / L0

L255: Luminance of gray level 255, L0: Luminance of gray level 0

Note (5) Definition Of Response Time (T_R, T_F)

Figure 6 Definition of Response Time



Note (6) Definition Of Luminance Uniformity (Ref.: Active Area)

6bit: Measure the luminance of gray level 63 at 9 points.

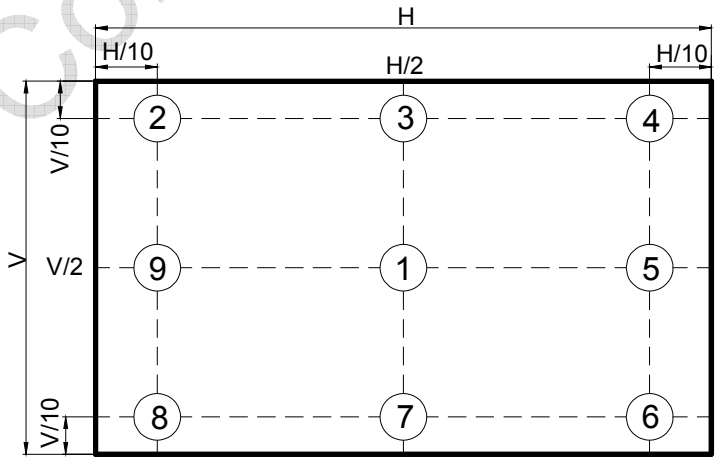
Luminance Uniformity= Min.(L1, L2, ... L9) / Max.(L1, L2, ... L9)

8bit: Measure the luminance of gray level 255 at 9 points.

Luminance Uniformity= Min.(L1, L2, ... L9) / Max.(L1, L2, ... L9)

H—Active Area Width, V—Active Area Height, L—Luminance

Figure 7 Measurement Locations of 9 Points



4.0 Electrical Characteristics

4.1 Interface Connector

Table 3 Signal Connector Type

Item	Description
Manufacturer / Type	MSB24013P20
LED Driver Connector	MSB24038P4

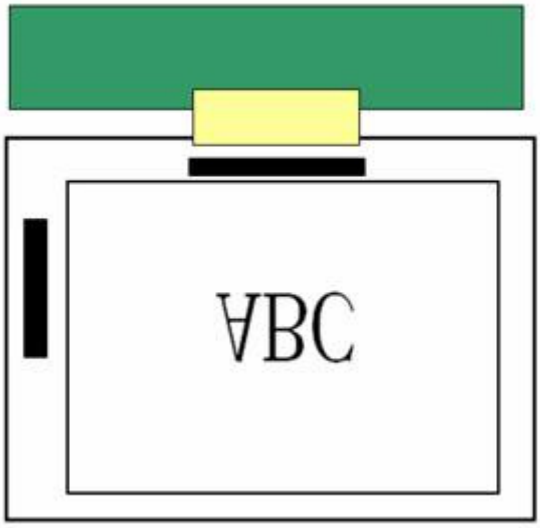
Table 4-1 LVDS&POWER Connector Pin Assignment

Pin No.	Symbol	Description	Remarks
1	VDD	Power Supply, 3.3V (typical)	
2	VDD	Power Supply, 3.3V (typical)	-
3	UD	Vertical Reverse Scan control.	-
4	LR	Horizontal Reverse Scan control	-
5	RxIN1-	-LVDS differential data input (R0-R5,G0)	-
6	RxIN1+	+LVDS differential data input (R0-R5,G0)	-
7	GND	Ground	-
8	RxIN2-	-LVDS differential data input (G1-G5,B0-B1)	-
9	RxIN2+	+LVDS differential data input (G1-G5,B0-B1)	-
10	GND	Ground	-
11	RxIN3-	-LVDS differential data input (B2-B5,HS,VS,DE)	-
12	RxIN3+	+LVDS differential data input (B2-B5,HS,VS,DE)	-
13	GND	Ground	-
14	RxCLKIN-	-LVDS differential clock input	-
15	RxCLKIN+	+LVDS differential clock input	-
16	GND	Ground	-
17	SEL68	6/8 bits LVDS data input selection(H:8bit L/NC:6bit)	H:3.3V L:0V
18	BIST	H:BIST MODE, L/NC: Normal MODE	-
19	RxIN4-	-LVDS differential data input (R6-R7,G6-G7,B6-B7)	-
20	RxIN4+	+LVDS differential data input (R6-R7,G6-G7,B6-B7)	-

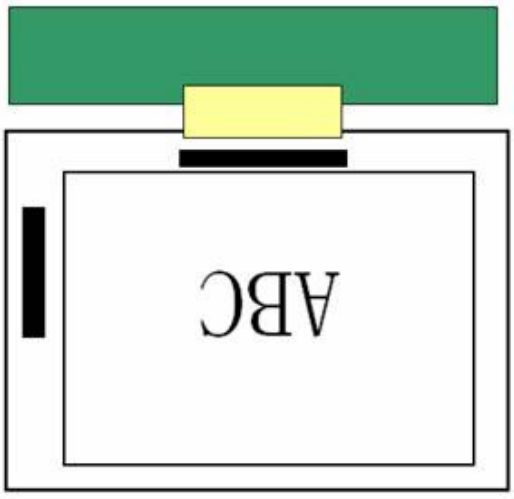
Table 4-2 LED Driver Connector Pin Assignment

Pin No.	Symbol	Description	Remarks
1	VLED	LED Driver Power Supply, 12V (typical)	
2	GND	Ground	-
3	EN	LED Driver Enable	-
4	PWM	PWM Signal input	-

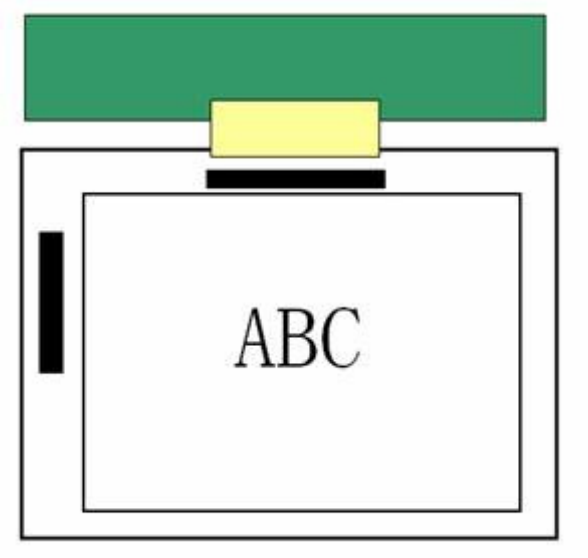
(1) UD= HIGH,LR=LOW



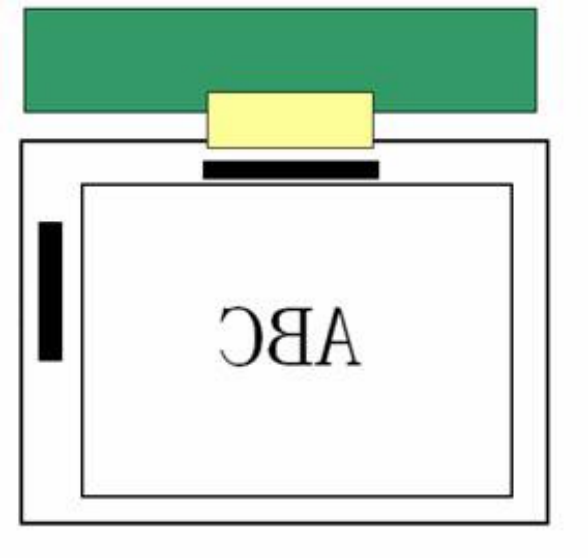
(2) UD= HIGH,LR=HIGH



(3) UD= LOW,LR=LOW



(4) UD= LOW,LR=HIGH



4.2 Signal Electrical Characteristics

4.2.1 Signal Electrical Characteristics For LVDS Receiver

The built-in LVDS receiver is compatible with (ANSI/TIA/TIA-644) standard.

Table 5 LVDS Receiver Electrical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Differential Input High Threshold	Vth	-	-	+100	mV	V_{CM}=+1.2V
Differential Input Low Threshold	Vtl	-100	-	-	mV	V_{CM}=+1.2V
Magnitude Differential Input Voltage	$\|V_{ID}\|$	200	-	600	mV	-
Common Mode Voltage	V_{CM}	$\|V_{ID}\|/2$	1.2	1.4	V	V_{th}-V_{tl}=200mV
Common Mode Voltage Offset	ΔV_{CM}	-50	-	+50	mV	V_{th}-V_{tl}=200mV

Note (1) Input signals shall be low or Hi- resistance state when VDD is off.

Note (2) All electrical characteristics for LVDS signal are defined and shall be measured at the interface connector of LCD.

Figure 8 Voltage Definitions

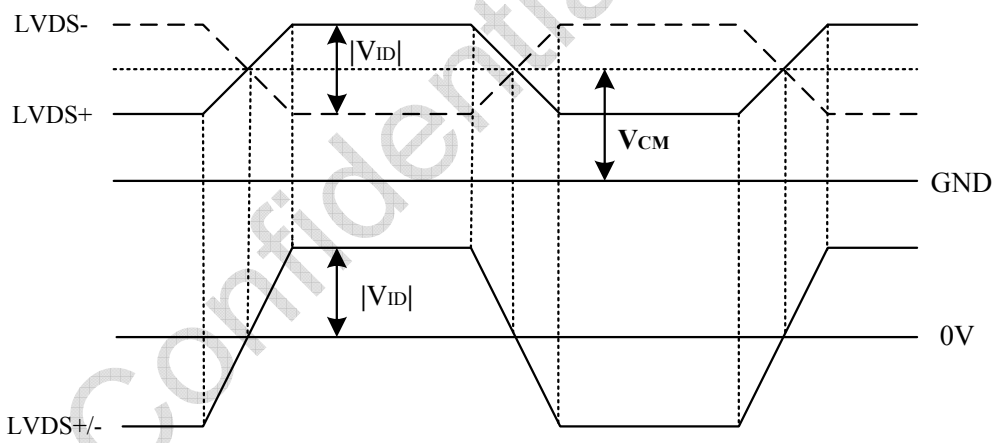


Figure 9 Measurement System

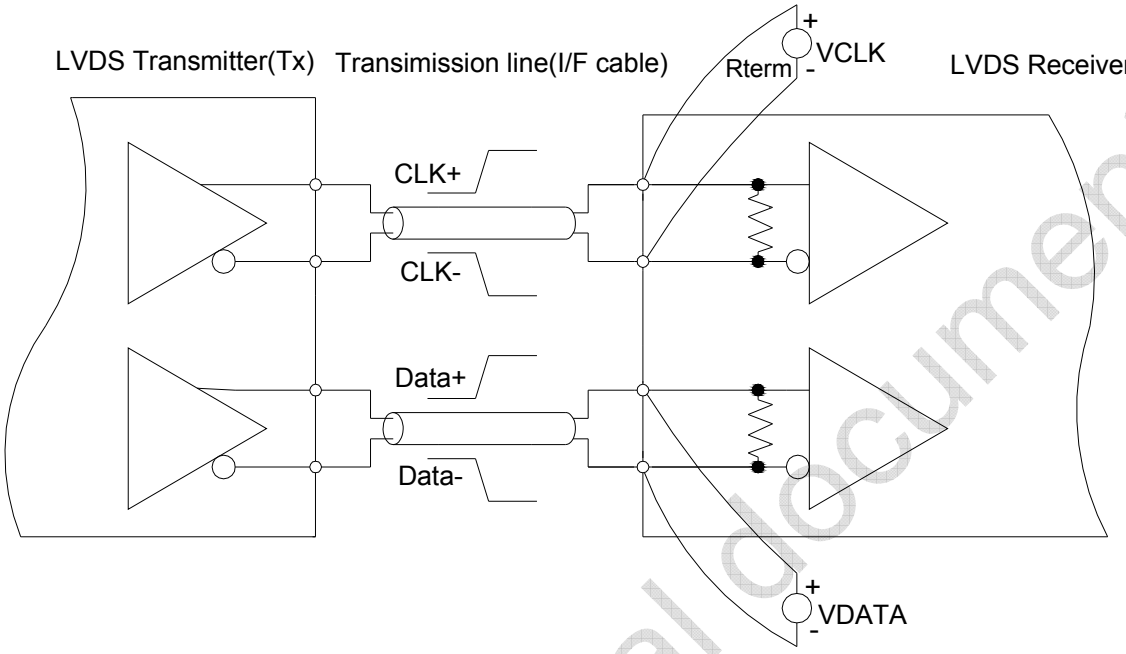
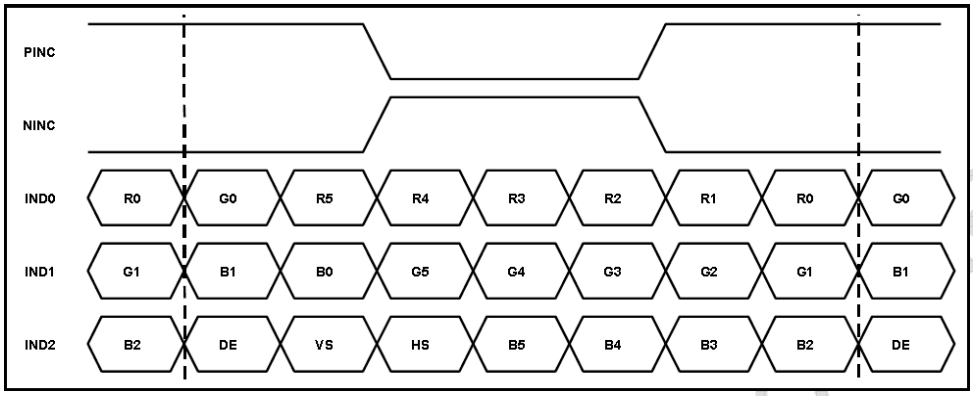
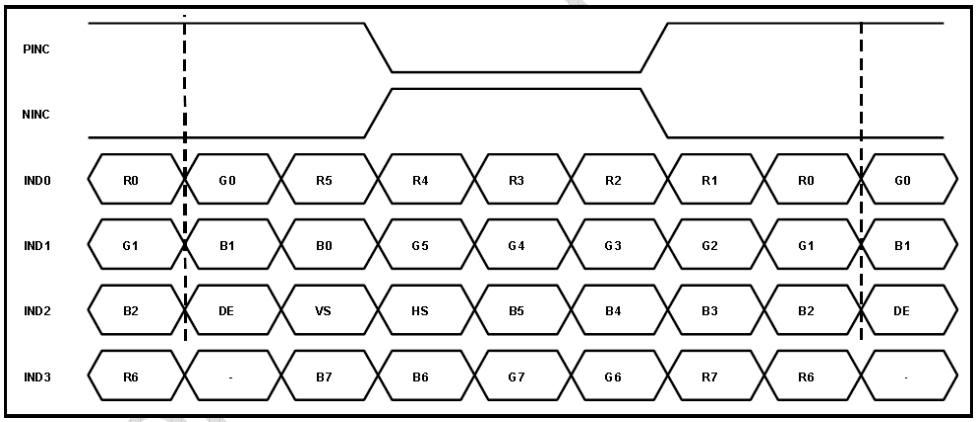


Figure 10 Data Mapping

Single 6 bit LVDS input



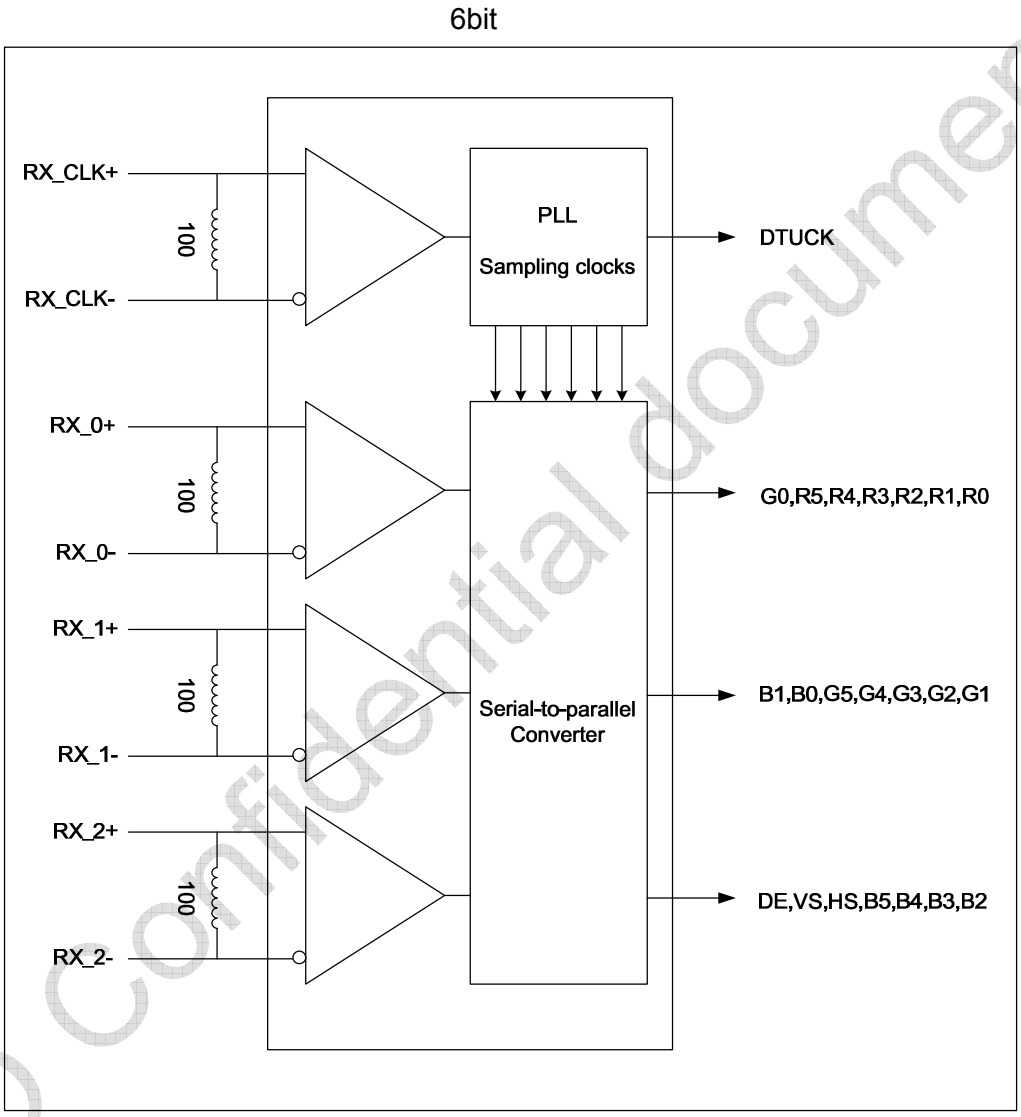
Single 8 bit LVDS input



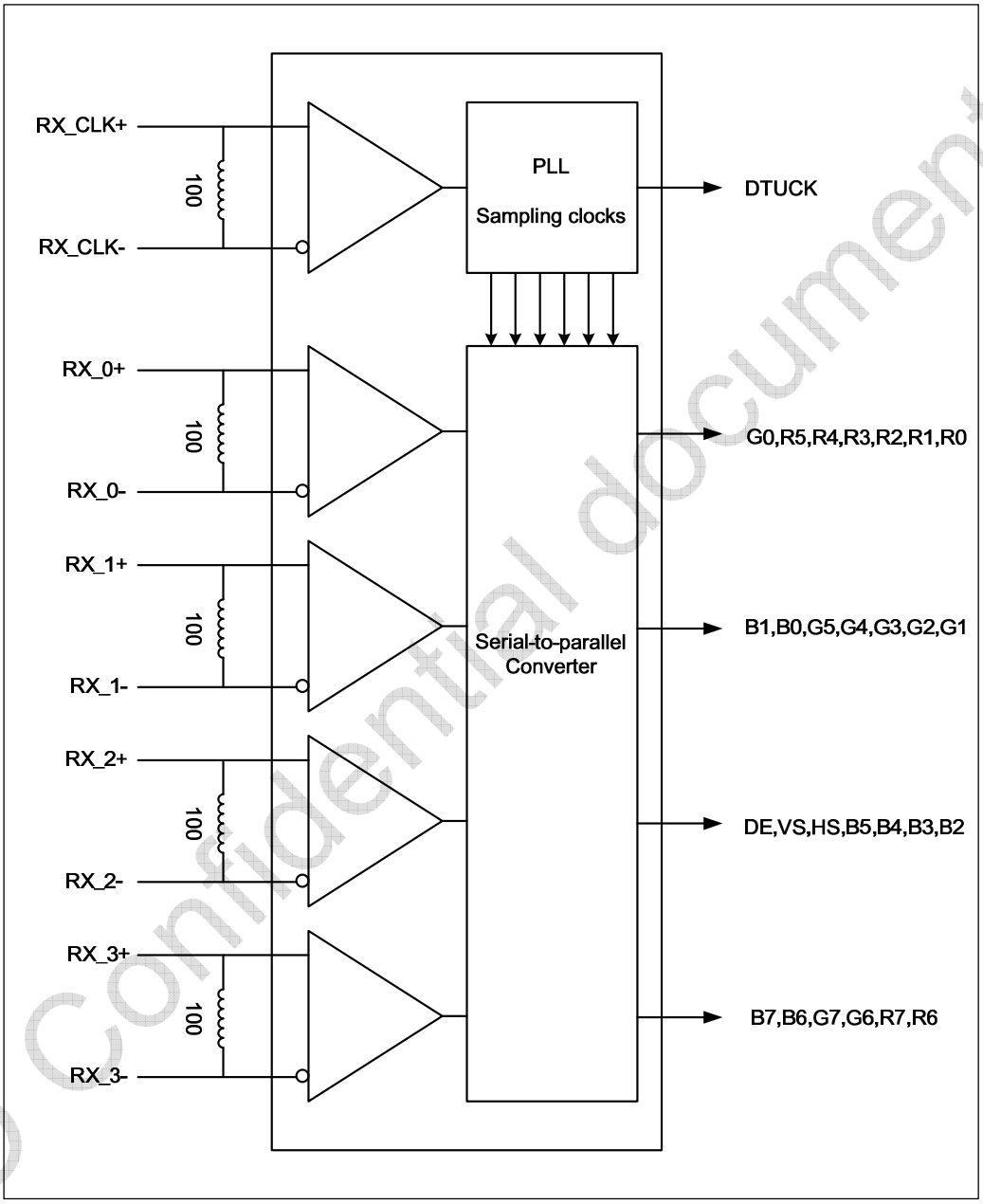
4.2.2 LVDS Receiver Internal Circuit

Figure 11 LVDS Receiver Internal Circuit shows the internal block diagram of the LVDS receiver. This LCD module equips termination resistors for LVDS link.

Figure 11 LVDS Receiver Internal Circuit



8bit



4.3 Interface Timings

Table 6 Interface Timings

Parameter	Symbol	Min.	Typ.	Max.	Unit
LVDS Clock Frequency	Fclk	32.6	39.6	62.4	MHz
H Total Time	HT	890	1000	1300	Clocks
H Active Time	HA	800	800	800	Clocks
V Total Time	VT	610	660	800	Lines
V Active Time	VA	600	600	600	Lines
Frame Rate	FV	55	60	65	Hz

Note：Htotal*Vtotal*Frame Rate＜67.6MHz

4.4 Input Power Specifications

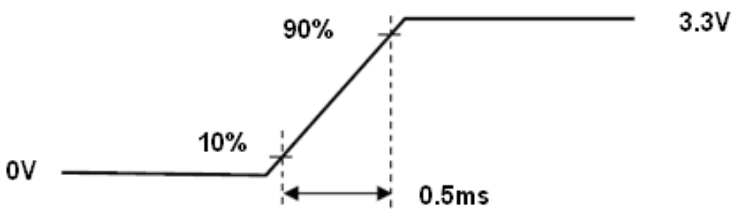
Input power specifications are as follows.

Table 7 Input Power Specifications

Parameter		Symbol	Min.	Typ.	Max.	Unit	Note
System Power Supply							
LCD Drive Voltage (Logic)		V_{DD}	3.0	3.3	3.6	V	(2), (4)
VDD Current	black Pattern	I_{DD}	-	-	(0.097)	A	(3),(4)
VDD Power Consumption	black Pattern	P_{DD}	-	-	(0.35)	W	
Rush Current		I_{Rush}	-	-	2	A	(1),(4),(5)
Allowable Logic/LCD Drive Ripple Voltage		V_{VDD-RP}	-	-	200	mV	(4)
LED Power Supply							
LED Input Voltage		V_{LED}	10.8	12	13.2	V	(4)(6)
LED Power Consumption		P_{LED}	-	-	(2.31)	W	
LED Forward Voltage		V_F	2.8	3.3	3.6	V	
LED Forward Current		I_F	-	17	-	mA	
PWM Signal Voltage	High	V_{PWM}	2.5	-	3.6	V	
	Low		0	-	0.5		
LED Enable Voltage	High	V_{LED_EN}	2.5	-	3.6	V	
	Low		0	-	0.5		
Input PWM Frequency		F_{PWM}	100	-	2,0000	Hz	
Duty Ratio		PWM	5	-	100	%	
LED Life Time		LT	(30,000)	(50,000)	-	Hours	(4)(7)

Note (1) Measure Condition

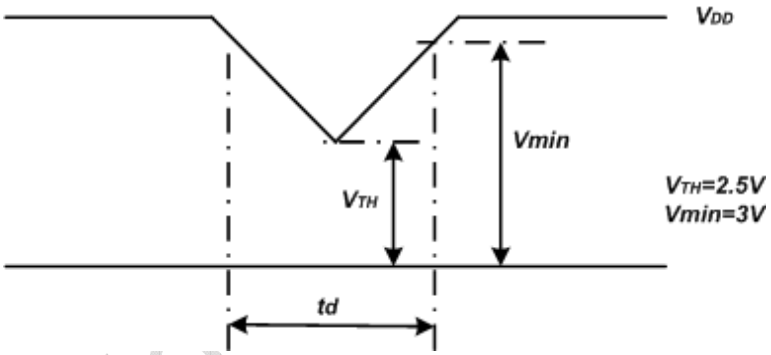
Figure 12 VDD Rising Time



Note (2) VDD Power Dip Condition

V_{TH}< V_{DD}≤ Vmin, td≤ 10ms (a time of the voltage return to normal), our panel can revive automatically.

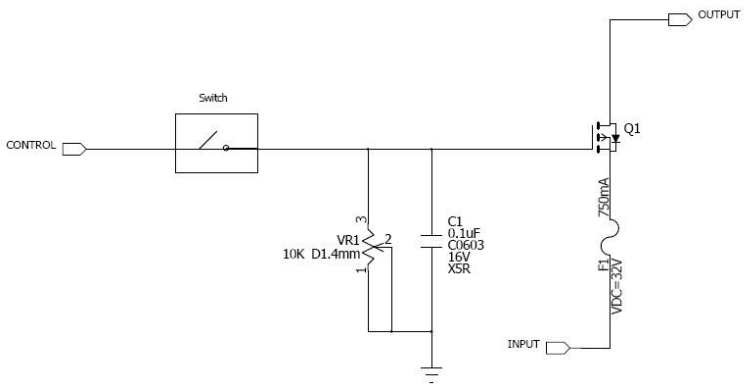
Figure 13 VDD Power Dip



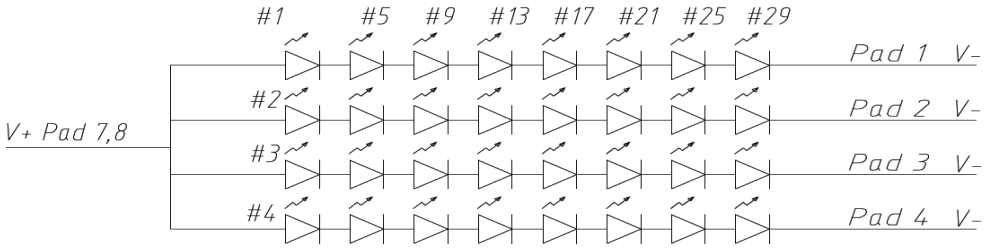
Note (3) Frame Rate=60Hz, VDD=3.6V, DC Current.

Note (4) Operating temperature 25℃, humidity 55%RH.

Note (5) The reference measurement circuit of rush current.



Note (6) Condition of LED



Note (7) The LED life time define as the estimated time to 50% degradation of initial luminous.

4.5 Power ON/OFF Sequence

Interface signals are also shown in the chart. Signals from any system shall be Hi- resistance state or low level when VDD voltage is off.

Figure 14 Power Sequence

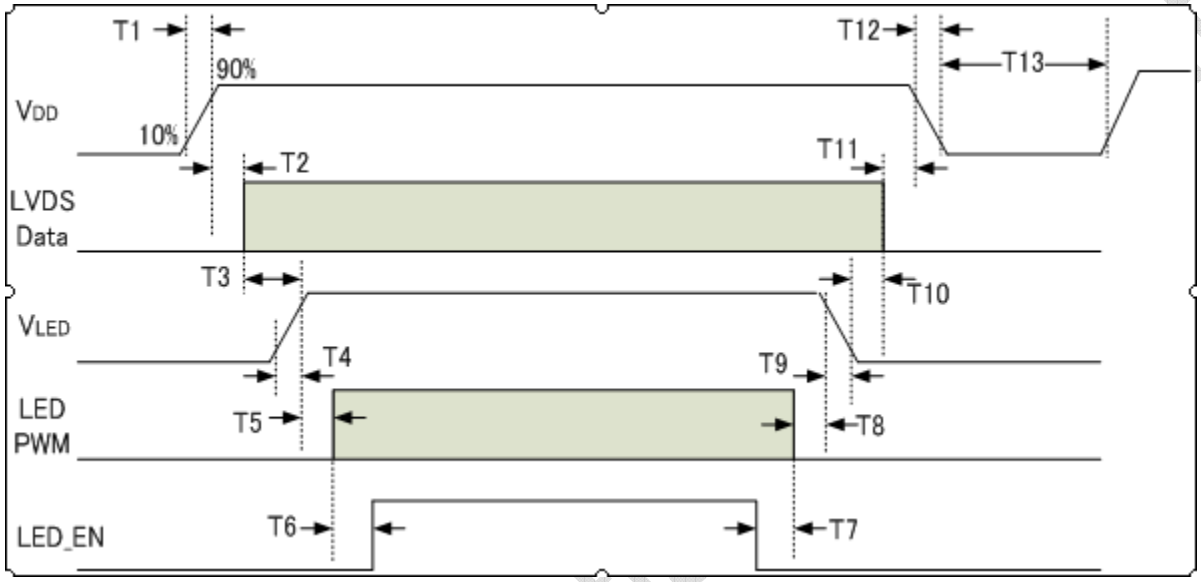


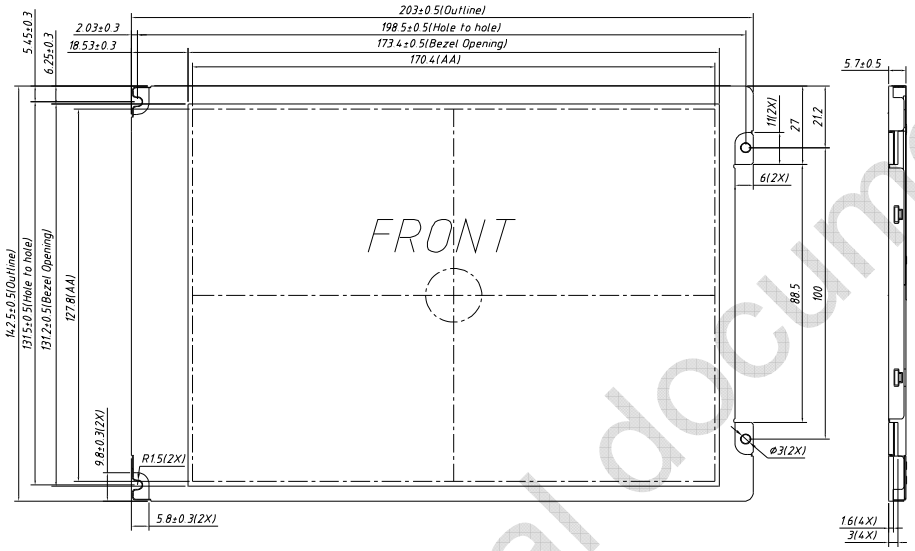
Table 8 Power Sequencing Requirements

Parameter	Symbol	Min.	Typ.	Max.	Unit
VIN Rise Time	T1	0.5	-	10	ms
VIN Good to Signal Valid	T2	30	-	90	ms
Signal Valid to Backlight On	T3	200	-	-	ms
Backlight Power On Time	T4	0.5	-	-	ms
Backlight VDD Good to System PWM On	T5	10	-	-	ms
System PWM ON to Backlight Enable ON	T6	10	-	-	ms
Backlight Enable Off to System PWM Off	T7	0	-	-	ms
System PWM Off to B/L Power Disable	T8	10	-	-	ms
Backlight Power Off Time	T9	1	10	30	ms
Backlight Off to Signal Disable	T10	200	-	-	ms
Signal Disable to Power Down	T11	0	-	50	ms
VIN Fall Time	T12	1	10	30	ms
Power Off	T13	500	-	-	ms

5.0 Mechanical Characteristics

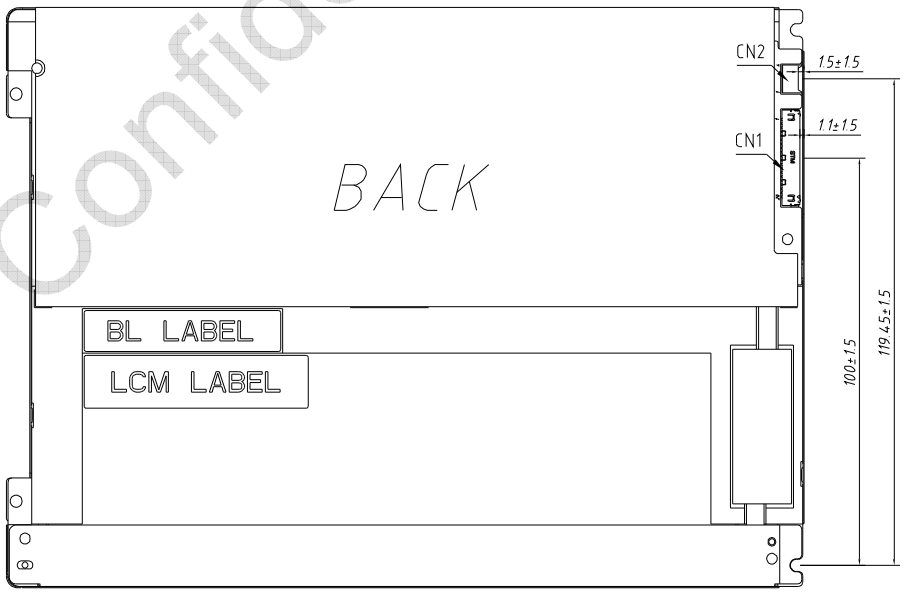
5.1 Outline Drawing

Figure 15 Outline Drawing (Front Side)



Note (1)Unnoted tolerance :±0.5mm.

Figure 16 Reference Outline Drawing (Back Side)

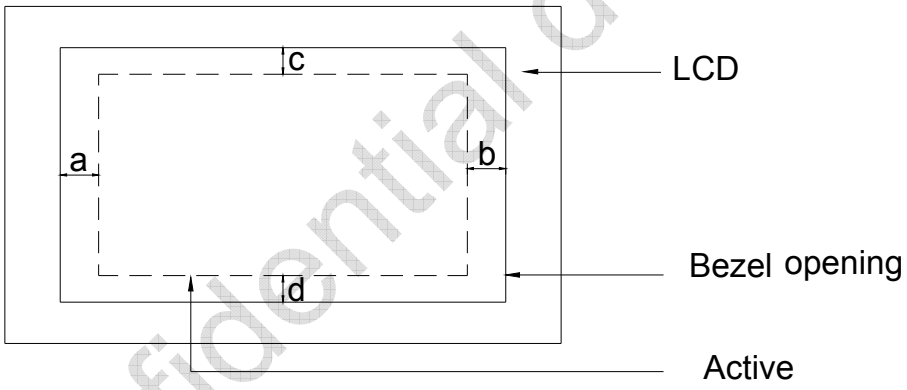


5.2 Dimension Specifications

Table 9 Module Dimension Specifications

Item	Min.	Typ.	Max.	Unit
Width	(202.5)	(203)	(203.5)	mm
Height	(142.5)	(143)	(143.5)	mm
Thickness（with PCBA）	(5.2)	(5.7)	(6.2)	mm
Weight	-	-	(200)	g
BM: | a-b | & | c-d |	-	-	1.0	mm

Figure 17 BM Area



6.0 Reliability Conditions

Item		Package	Test Conditions		Note
Low Temperature Operating Test		Module	-20℃, 500 hours		(1),(2),(3),(4)
High Temperature Operating Test		Module	70℃, 500 hours		(1),(2),(3),(4)
High Temperature Storage Test		Module	80℃, 500 hours		(1),(2),(4)
Low Temperature Storage Test		Module	-30℃, 500 hours		(1),(2),(4)
High Temperature/High Humidity Operating Test		Module	50℃, 85%RH, 500 hours		(1),(2),(3),(4)
Shock Non-operating Test		Module	100G,6ms,X Y Z×2faces×3times, Total 18 times		(4)
Vibration Non-operating Test		Module	half-sine Frequency: 8Hz ~ 33Hz Stroke: 1.3mm Sweep: 2.9G 33.3Hz ~ 400Hz X,Z Cycle : 15 minutes 2 hrs for each direction of X,Z ; 4 hours for Y direction		(4)
ESD Test	Operating	Module	Contact	± 8 KV, 150pF(330Ohm)	(5)
			Air	± 15 KV, 150pF(330Ohm)	
	Non-operating		Contact	± 10 KV, 150pF(330Ohm)	
			Air	± 20 KV, 150pF(330Ohm)	

Note (1) All the judgments are under room temperature and the sample need to be static more than 2 hours in the room temperature before judge.

Note (2) During measurement, the condensation water or remains shall not be allowed.

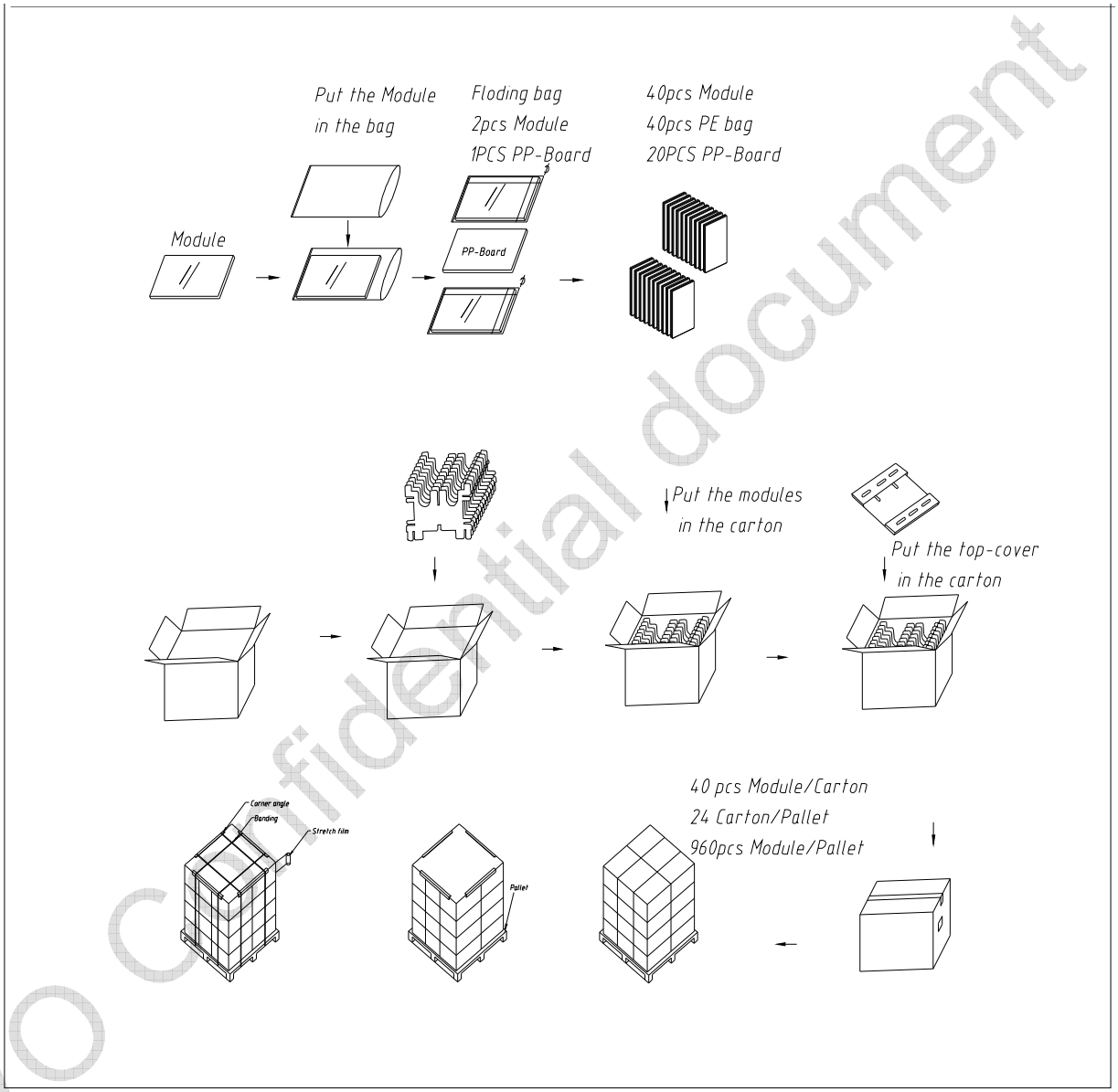
Note (3) In operating test, the backlight voltage and current must be in speccfication.

Note (4) There is no display function issue occurred, all the cosmetic specification is judged before the reliability stress.

Note (5) In case of malfunction defect caused by ESD damage. If it would be recovered to normal state after resetting, it would be judge as pass.

7.0 Package Specification

Figure 18 Packing Method



8.0 Lot Mark

TBD

9.0 General Precaution

9.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

9.2 Handling Precaution

(1) Please mount LCD module by using mounting holes arranged in four corners tightly.

(2) Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. IVO does not warrant the module, if customers disassemble or modify the module.

(3) If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin. If liquid crystal contacts mouth or eyes, rinse out with water immediately. If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.

(4) Disconnect power supply before handling LCD module.

(5) Refrain from strong mechanical shock and /or any force to the module.

(6) Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts parameters, environmental temperature; etc otherwise LCD module may be damaged. It's recommended employing protection circuit for power supply.

(7) Do not touch, push or rub the polarizer with anything harder than HB pencil lead. Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.

(8) When the surface is dusty, please wipe gently with absorbent cotton or other soft material. When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzene or other adequate solvent.

(9) Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.

(10) Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.

(11) Because LCD module uses CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge, please be careful with electrostatic discharge. Persons who handle the module should be grounded through adequate methods.

(12) Do not adjust the variable resistor located on the module.

9.3 Storage Precaution

(1) Please do not leave LCD module in the environment of high humidity and high temperature for a long time.

(2) The module shall not be exposed under strong light such as direct sunlight.Otherwise, display characteristics may be changed.

(3) The module should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light in storage.

9.4 Operation Precaution

(1) Do not connect or disconnect the module in the "Power On" condition.

(2) Power supply should always be turned on/off by "Power On/Off Sequence".

(3) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

(4) After installation of the TFT module into an enclosure, do not twist nor bend the TFT module even momentary. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT module from outside. Otherwise the TFT module may be damaged.

9.5 Others

(1) Ultra-violet ray filter is necessary for outdoor operation.

(2) Avoid condensation of water which may result in improper operation or disconnection of electrode.

(3) If the module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.

(4) This module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.

9.6 Disposal

When disposing LCD module, obey the local environmental regulations.